

FIGURE 1A

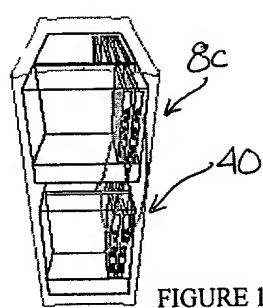


FIGURE 1B

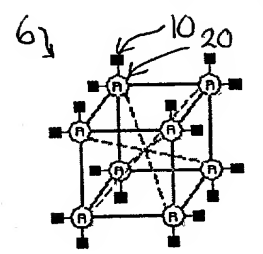


FIGURE 2A

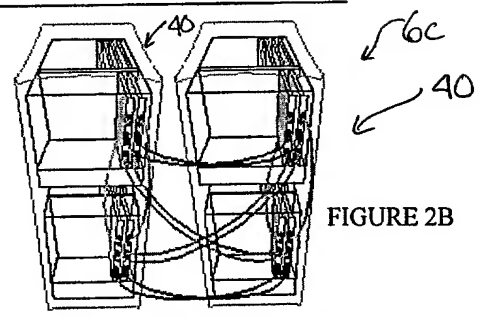


FIGURE 2B

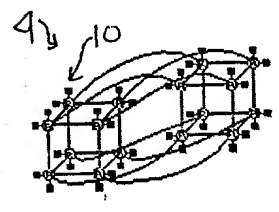


FIGURE 3A

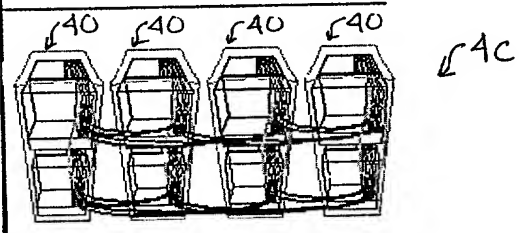


FIGURE 3B

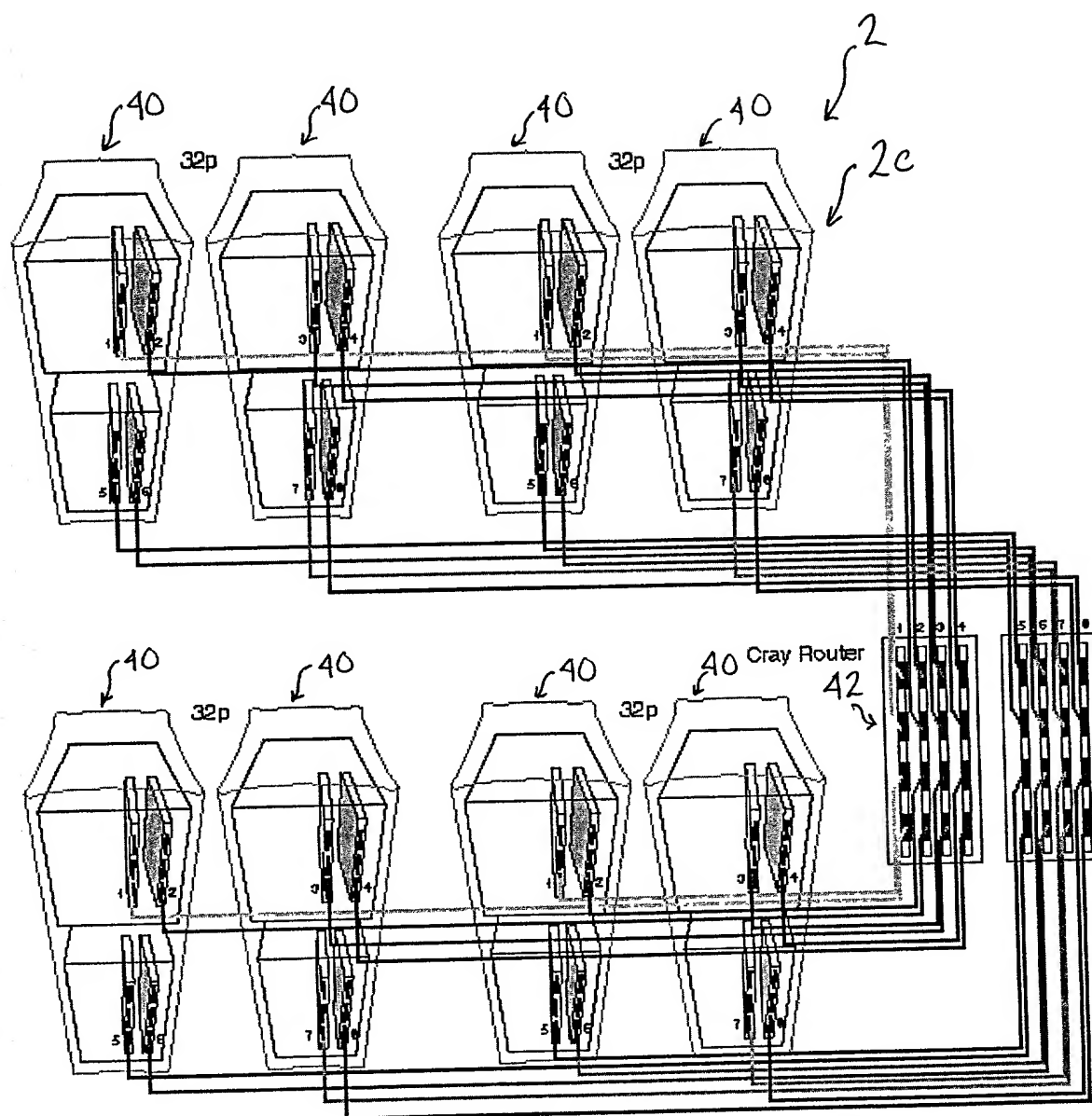


FIGURE 4

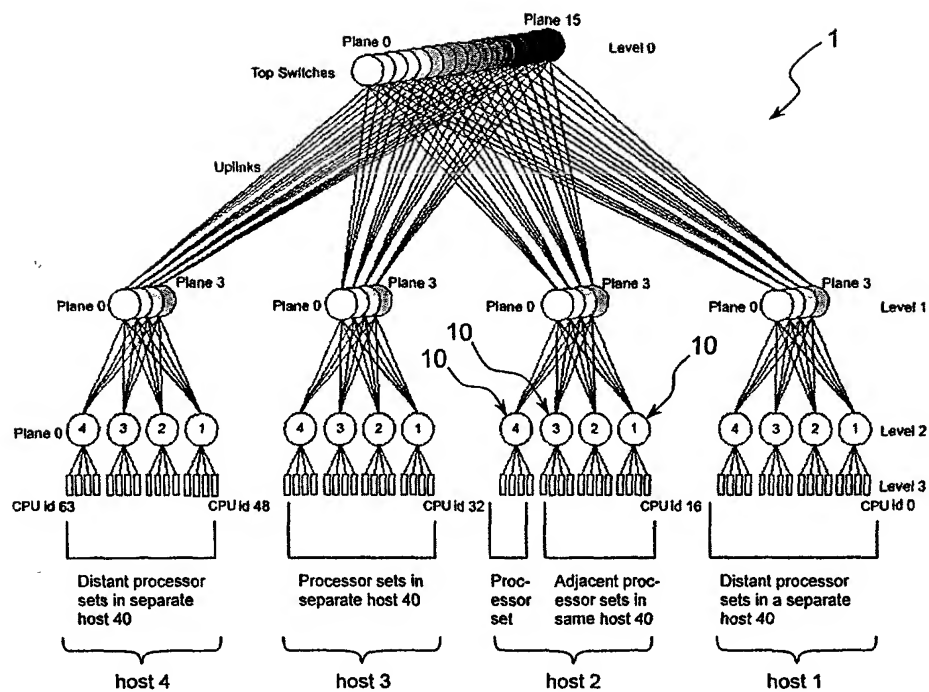


FIGURE 5

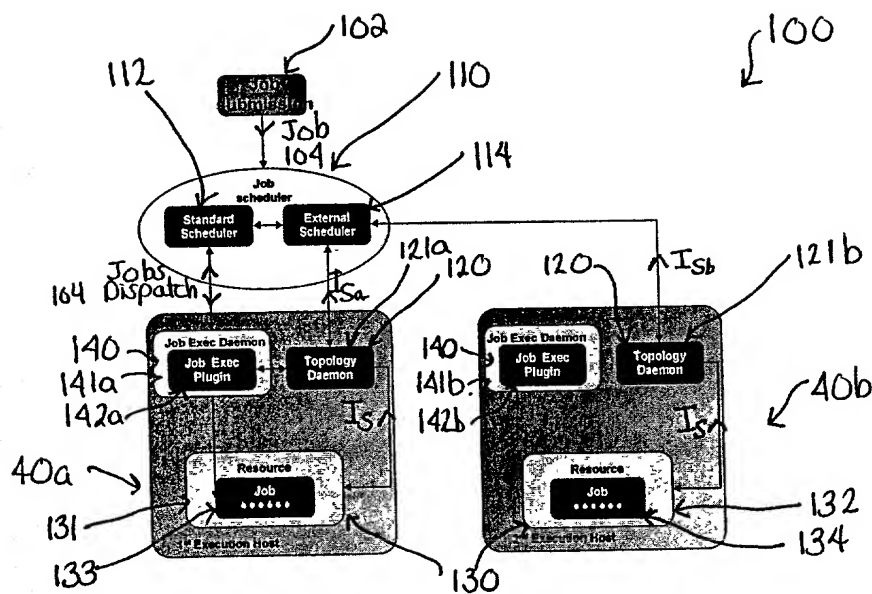


FIGURE 6

Interconnection 20

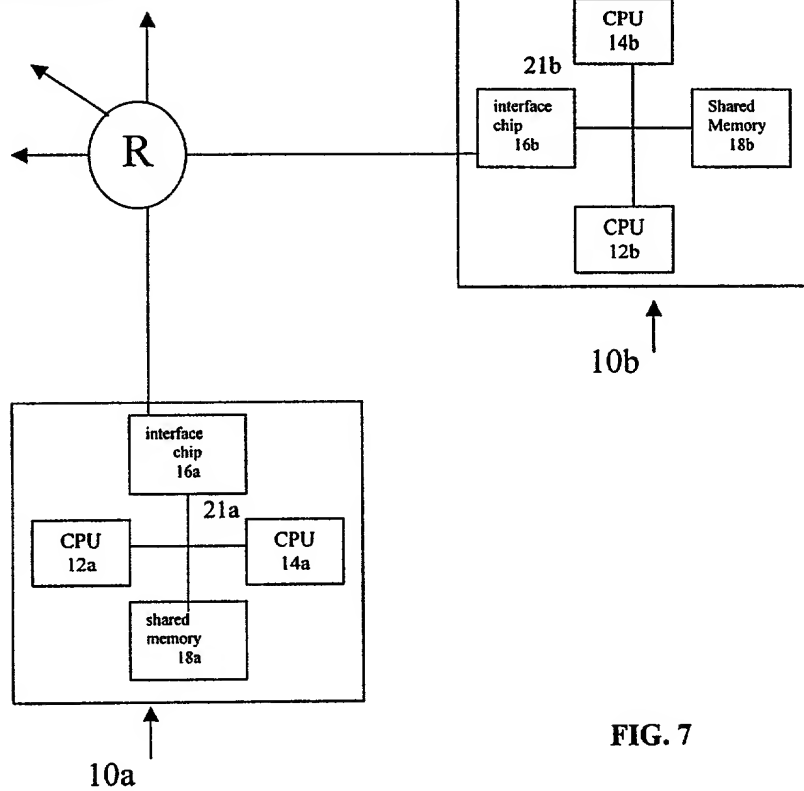
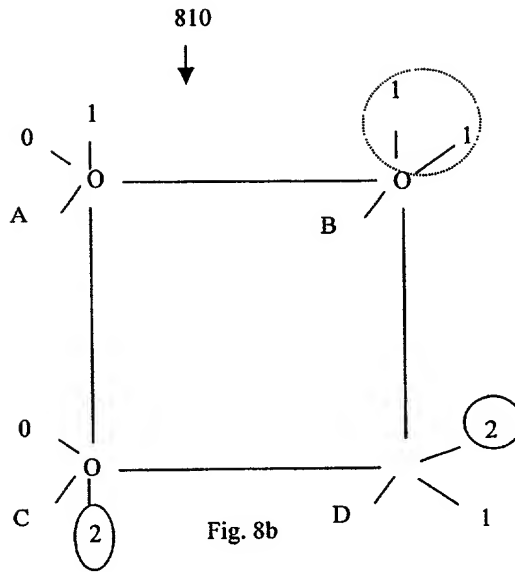
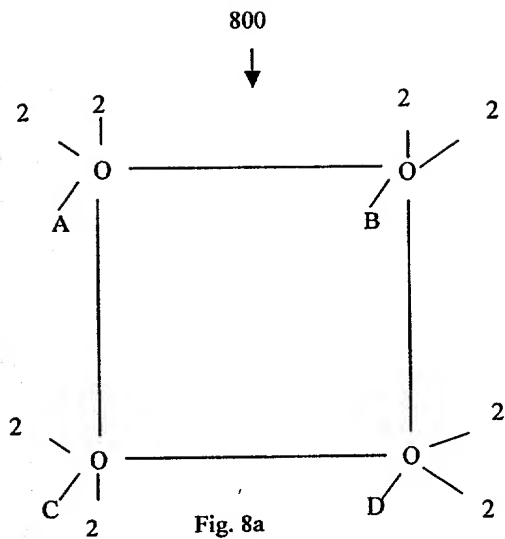
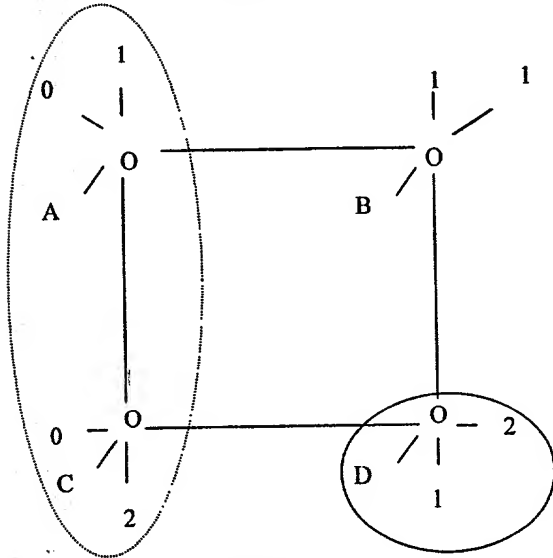


FIG. 7

2047373 2047373 2047373



Job needs 2 processors



Job needs 3 processors

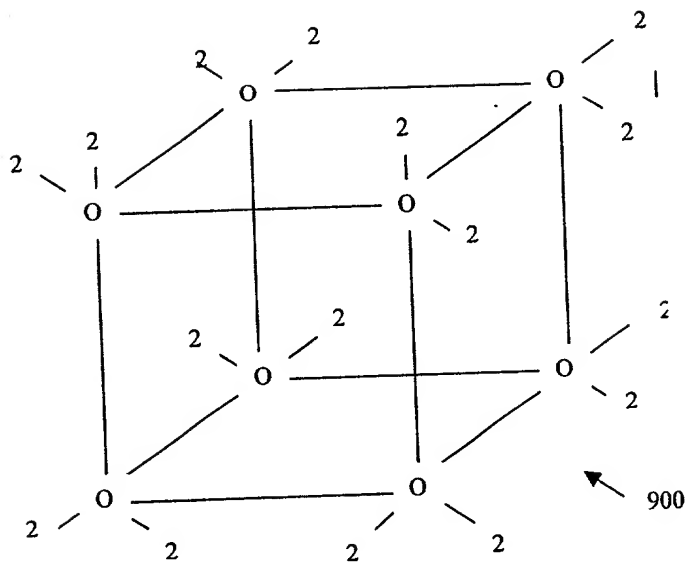


Fig. 9a

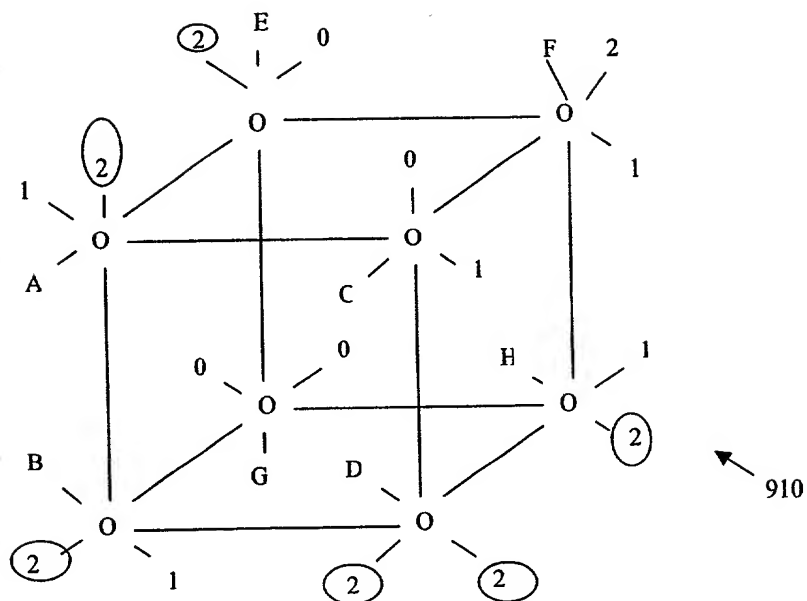


Fig. 9b

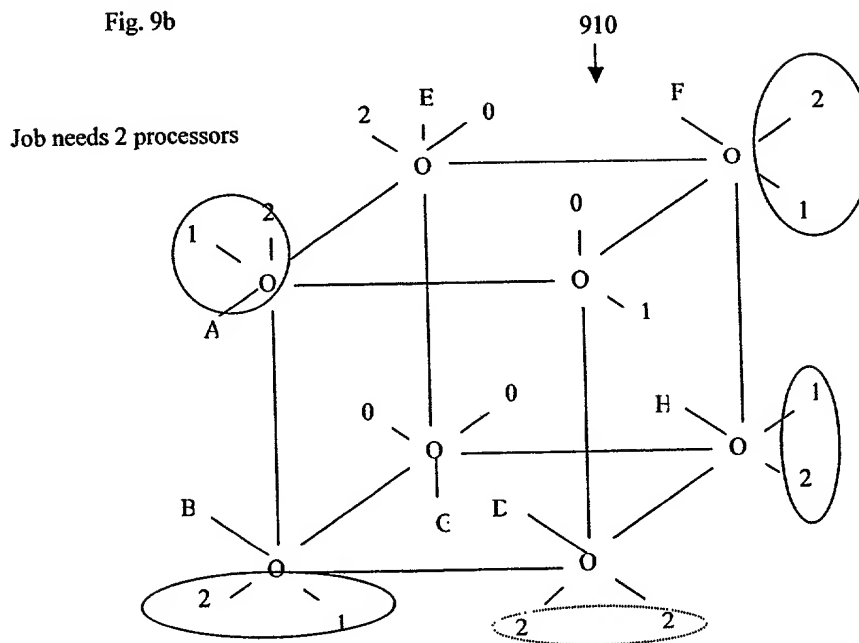
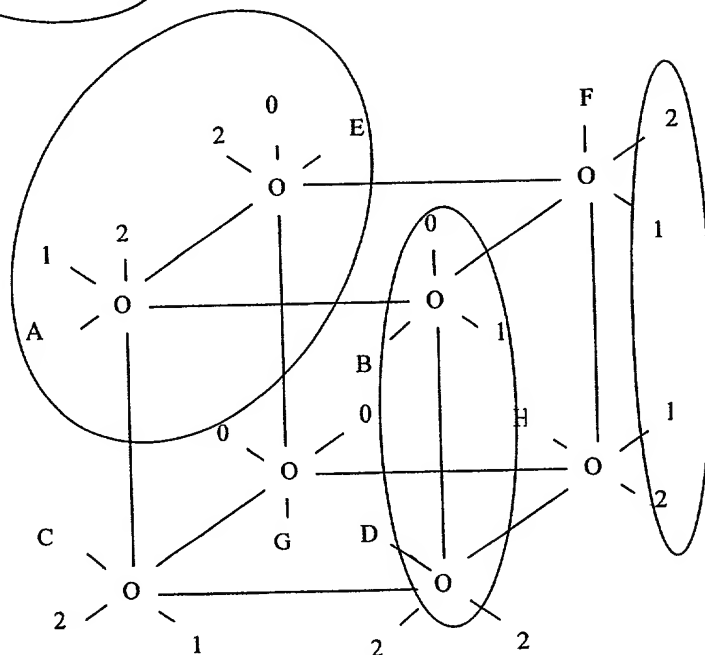
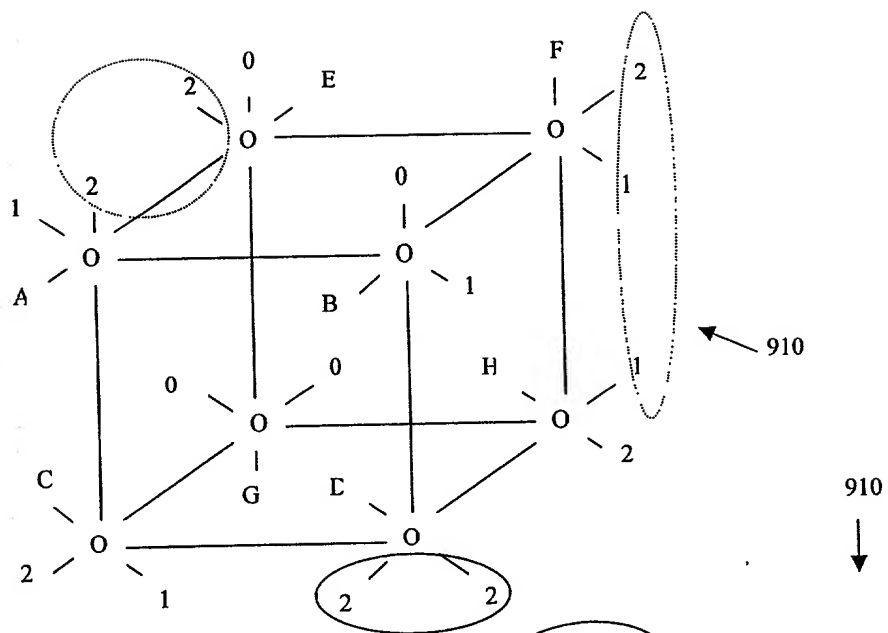
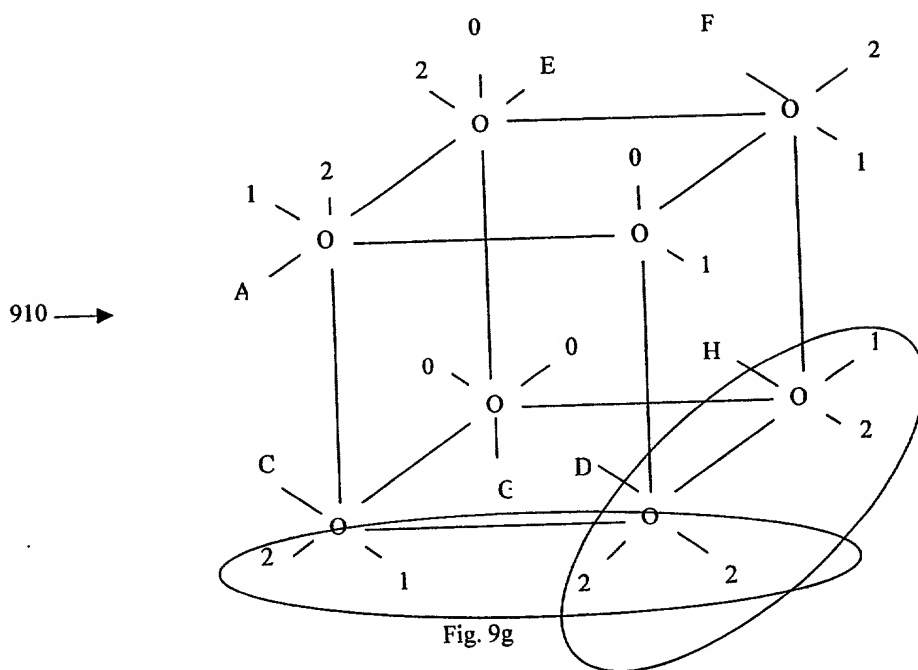


Fig. 9c

Job needs 3 processors



Job needs 6 processors



Job needs 7 processors

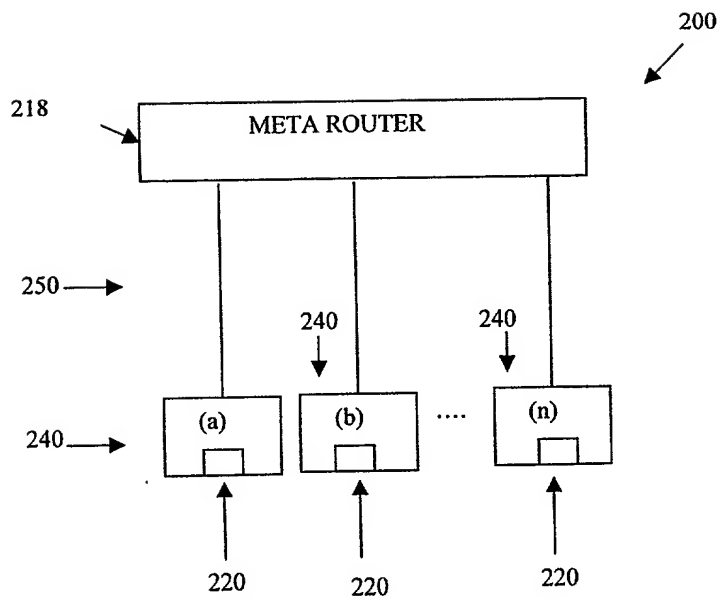


Fig. 10